

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	0	(semiconductor with devcie) and (mount or mounted) and (slant or slanted)	USPAT
2	BRS	L2	1	(semiconductor with devcie) and (mount or mounted)	USPAT
3	BRS	L3	948	(semiconductor same surface) and (alignment same groove)	USPAT
4	BRS	L4	529	3 and (mount or mounted)	USPAT
5	BRS	L5	328	4 and (optic or optical)	USPAT
6	BRS	L6	293	5 and (position or positioning)	USPAT
7	BRS	L7	17	6 and (slant or slanted)	USPAT
8	BRS	L8	14	7 and (optical same semiconductor)	USPAT
9	BRS	L9	12	8 and(connect or join or connected or joined)	USPAT

	Time Stamp	Comments	Error Definition	Errors
1	2003/11/12 08:20			0
2	2003/11/12 08:20			0
3	2003/11/12 08:26			0
4	2003/11/12 08:26			0
5	2003/11/12 08:27			0
6	2003/11/12 08:27			0
7	2003/11/12 08:27			0
8	2003/11/12 08:28			0
9	2003/11/12 08:29			0